

[Request information](#)

PRODUCT SPECIFICATION

TFT-LCD MODULE

Module No: 100349-01

☒ Preliminary Specification

☐ Approval Specification

Designed by	Checked by	Approved by

Final Approval by Customer

Approved by	Comment
For more information, contact us:	
WWW.DCLTEK.COM 1-800-407-8917 SALES@DCLTEK.COM	

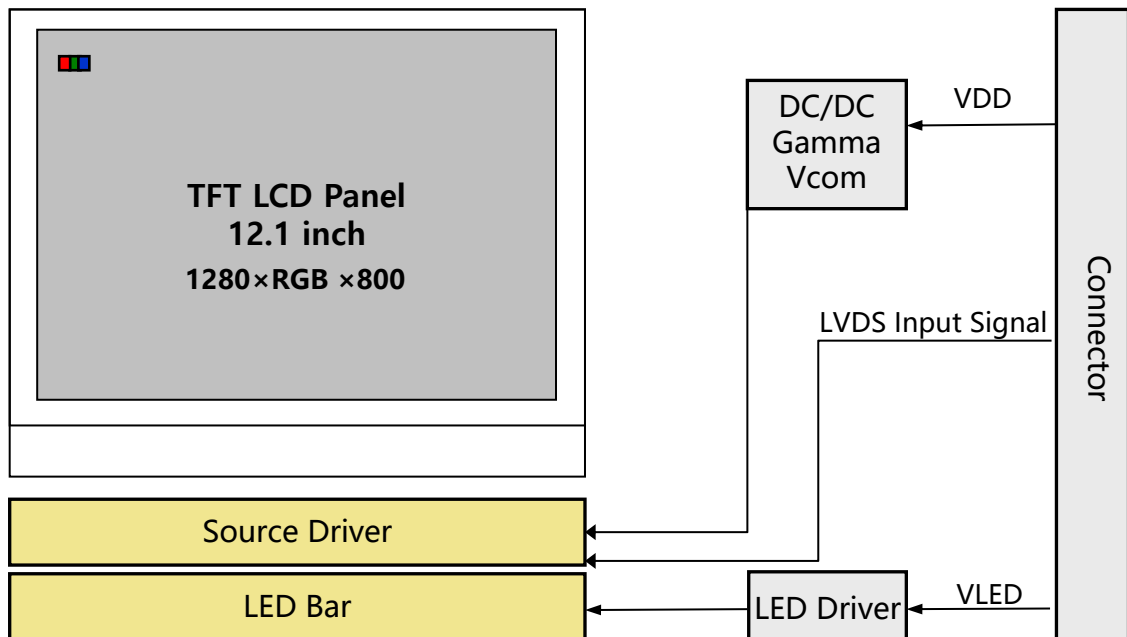
1.0 Document Revision History

Version	Contents	Date	Note
V0	First Specification Release	2023.06.16	

1.1 GENERAL DESCRIPTION

1.0.1 Introduction

100349-01 is a color active matrix TFT LCD panel using amorphous silicon TFT's as an active switching devices. This panel has a 12.1 inch diagonally measured active area with XGA resolutions (1280 horizontal by 800 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M(8bits) colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.0.2 Features

- 8-bit color depth, display 16. 7M colors
- LVDS Interface with 1pixel / clock
- DE (Data Enable) only
- Reverse Type
- Thin and light weight
- High luminance and contrast ratio, low reflection and wide viewing angle
- RoHS Compliant
- 7*24hrs usage support with dynamic video 或者7*18hrs
- Landscape and Portrait usage support

1.0.3 Application

- For industrial control white Treadmill
- Portable display white refrigerator
- Washing machine and coffee-machine etc. white goods

1.0.4 General Specification

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active Area	261.12 (H) × 163.20(V)	mm	
Number Of Pixels	1280(H) × 800(V) (1 pixel = R + G + B dots)	pixels	
Pixel Pitch	0.204(H) × 0.204(V)	mm	
Pixel Arrangement	RGB Vertical stripe		
Display Mode	Normally Black		
Display Colors	16.7M	colors	
Surface Treatment	AG42		
Contrast Ratio	1200 typ		
Viewing Angle(CR>10)	U/D/L/R Tpy: > 85/85/85/85	deg.	
Response Time	30typ/35max	ms	
Color Gamut	NTSC 72%typ		
Transmittance	Typ. 5.0%	%	基于12.1WX BLU
Power Consumption	1.122	W	
Outline Dimension	278.00 (H) × 184.00(V)×6.5(T)	mm	
Weight	TBD	g	
Back Light	36 White LED	-	

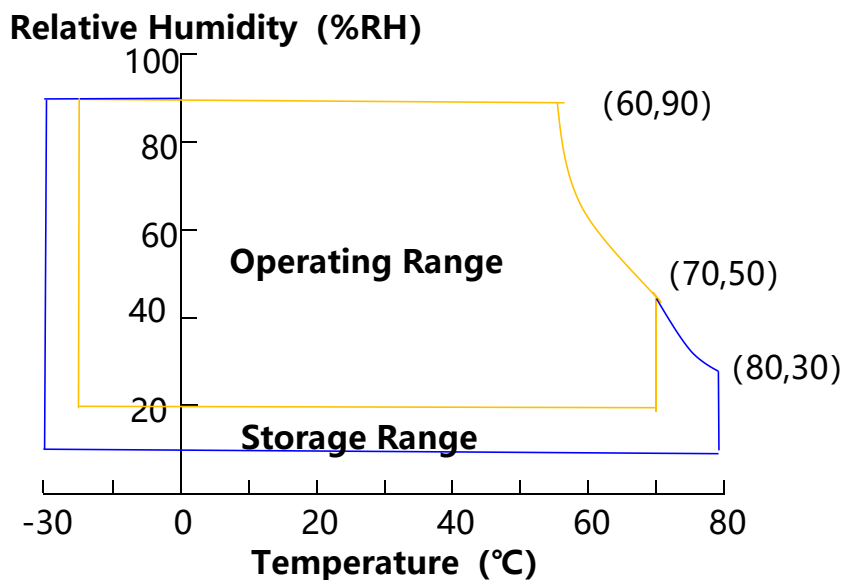
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Environment Absolute Maximum Ratings> [Ta = 25±2 °C]

Parameter	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	VDD	3.0	3.6	V	Ta = 25 °C
Operating Temperature	T _{OP}	-10	+60	°C	Note 1
Storage Temperature	T _{ST}	-20	+70	°C	
Operating Ambient Humidity	H _{op}	10	90	%RH	
Storage Humidity	H _{st}	10	90	%RH	

Note : 1) Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 °C max. and no condensation of water.



3.0 ELECTRICAL SPECIFICATIONS

3.0.1 TFT LCD Module

< Table 3. LCD Module Electrical Specifications >

[Ta =25±2 °C]

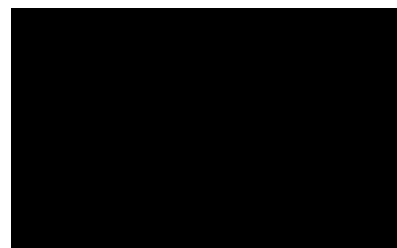
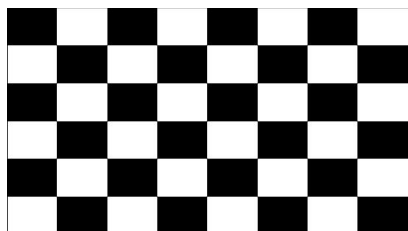
Parameter		Symbol	Values			Unit	Notes
			Min.	Typ.	Max.		
Power Supply Voltage		VDD	3.0	3.3	3.6	V	Note 1
Power Supply Ripple Voltage		VRP	-	-	100	mV	
Power Supply Current		IDD	100	130	160	mA	
Rush current		IRUSH	-	-	1	A	Note 2
LVDS Interface	Main link swing voltage	$ V_{ID} $	200	-	600	mV	
	Common mode voltage	Vcm	1	1.2	1.4	V	
Power Consumption		P _D	0.33	0.429	0.528	W	Note 1

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VBAT=3.3V, Frame rate $f_v=60\text{Hz}$ and Clock frequency = 72.4MHz. Test Pattern of power supply current

a) Typ : Mosaic 8 x 6 Pattern(L0/L255)

b) Max : Black



2. The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

3.0.2 Back-light Unit:

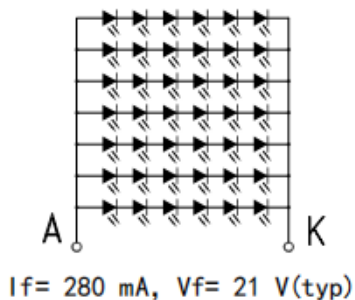
PARAMETE	Sym.	Min.	Typ.	Max.	Unit	Test Condition	Note
^R LED Current	IF	—	280	—	mA	—	—
LED Voltage	VF	—	21	—	V	I=280mA	—
Luminous	IV		350	—	Cd/m ²	I=280mA	—
Life Time		—	30000	—	Hr.	I=280mA	—
Color	White						

Note (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation

should be restricted to the conditions described under normal operating conditions.

(2) $T_a = 25 \pm 2^\circ\text{C}$

(3) Test condition: LED Current
180mA.



4.0 INTERFACE CONNECTION.

4.1 Electrical Interface Connection

The connector interface pin assignments are listed in Table 4

<Table 4 Pin Assignments for the Interface Connector>

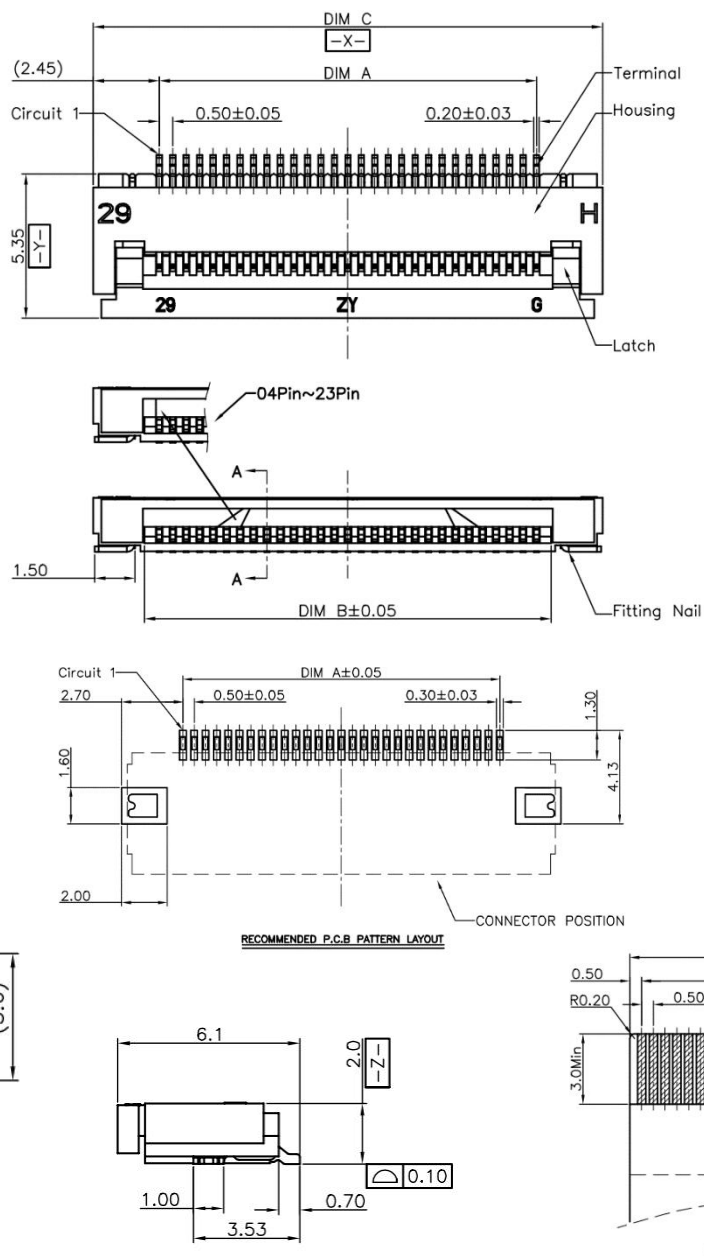
Terminal	Symbol	Functions
1	NC	No Connection.
2-4	VDD	Power Supply For LCD,VDD=3.3V.
5	NC	No Connection.
6-7	GND	Ground.
8	RIN0-	LVDSData differential signal input pins.
9	RIN0+	LVDS Data differential signal input pins.
10	GND	Ground.
11	RIN1-	LVDS Data differential signal input pins.
12	RIN1+	LVDS Data differential signal input pins.
13	GND	Ground.
14	RIN2-	LVDS Data differential signal input pins.
15	RIN2+	LVDS Data differential signal input pins.
16	GND	Ground.
17	LVDS_CLK-	LVDS CLOCK differential signal input pins.
18	LVDS_CLK+	LVDS CLOCK differential signal input pins.
19	GND	Ground.
20	RIN3-	LVDSData differential signal input pins.
21	RIN3+	LVDS Data differential signal input pins.
22	GND	Ground.
23-24	NC	No Connection.
25	GND	Ground.
26	SDA	Serial data input/output for I2C Interface.
27	SCL	Clock Input for I2C interface.
28	GND	Ground.
29-30	NC	No Connection.
31-33	GND	Ground.
34	NC	No Connection.
35	PWM	PWM dimming control input.
36	LED_EN	Chip enable (active high).
37	NC	No Connection.
38-40	VLED	LED Power supply input,VLED=5V-24V,12V TYP.

Rear view of LCM

AFC01-S40FCA-00(0.5inch , 2.0mm)



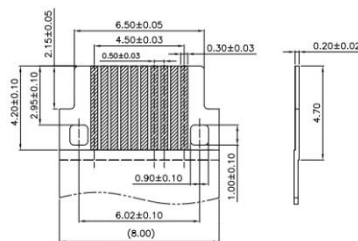
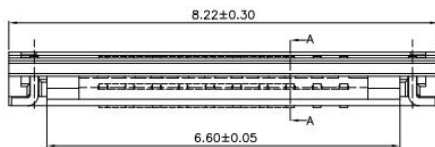
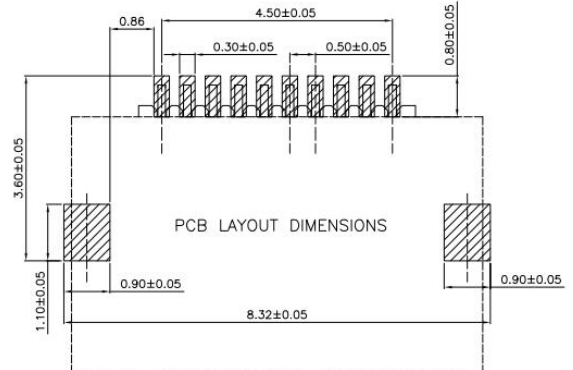
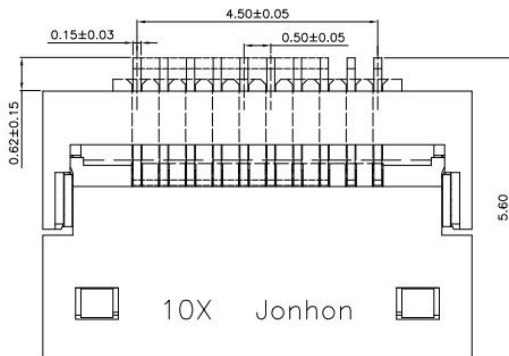
CNT Dimension



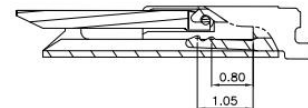
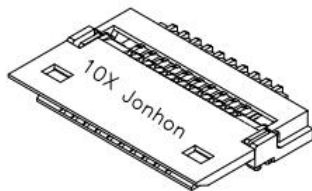
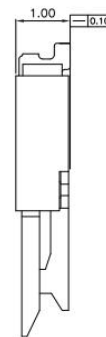
4.3 Dimension

CNT Dimension

-10pin Connector : MSK24022P10A



RECOMMENDED FFC DIMENSIONS
SCALE 0.5:1



SECTION A-A
SCALE 1:1

4.4 LVDS Interface

- LVDS Receiver : Timing Controller (LVDS Rx merged) / LVDS Data : Pixel Data
< Table 7. LVDS Input Data Mapping >

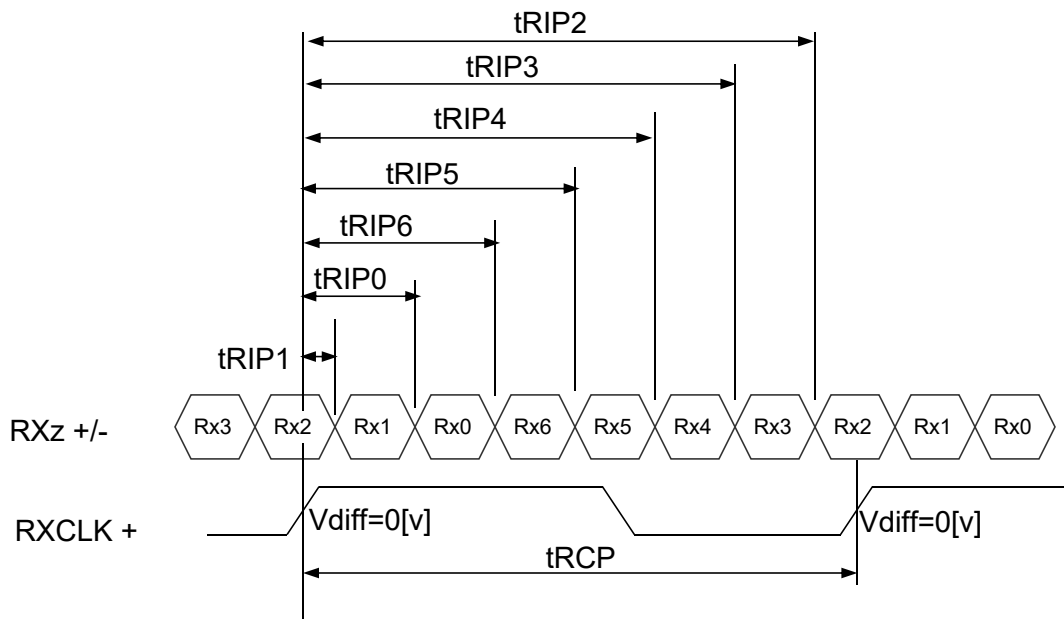
Channel No.	Data No.	8-bit LVDS Type
		VESA
0	Bit-0	R0
	Bit-1	R1
	Bit-2	R2
	Bit-3	R3
	Bit-4	R4
	Bit-5	R5
	Bit-6	G0
1	Bit-0	G1
	Bit-1	G2
	Bit-2	G3
	Bit-3	G4
	Bit-4	G5
	Bit-5	B0
	Bit-6	B1
2	Bit-0	B2
	Bit-1	B3
	Bit-2	B4
	Bit-3	B5
	Bit-4	HS
	Bit-5	VS
	Bit-6	DE
3	Bit-0	R6
	Bit-1	R7
	Bit-2	G6
	Bit-3	G7
	Bit-4	B6
	Bit-5	B7
	Bit-6	-

4.5 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 8.

<Table 8. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Typ	Max	Unit	Remark
CLKIN Period	tRCP	10	T	40	nsec	
Receiver Data Input Margin	tRMG	-0.45	-	+0.45	nsec	fCLKIN=80.9MHz
		-0.60	-	+0.60	nsec	fCLKIN=75MHz
Input Data 0	tRIP1	- tRMG	0.0	tRMG	Clock	
Input Data 1	tRIP0	T/7- tRMG	T/7	T/7+ tRMG	Clock	
Input Data 2	tRIP6	2 T/7- tRMG	2T/7	2T/7+ tRMG	Clock	
Input Data 3	tRIP5	3T/7- tRMG	3T/7	3T/7+ tRMG	Clock	
Input Data 4	tRIP4	4T/7- tRMG	4T/7	4T/7+ tRMG	Clock	
Input Data 5	tRIP3	5T/7- tRMG	5T/7	5T/7+ tRMG	Clock	
Input Data 6	tRIP2	6T/7- tRMG	6T/7	6T/7+ tRMG	Clock	



$$* V_{diff} = (RXz+) - (RXz-), \dots, (RXCLK+) - (RXCLK-)$$

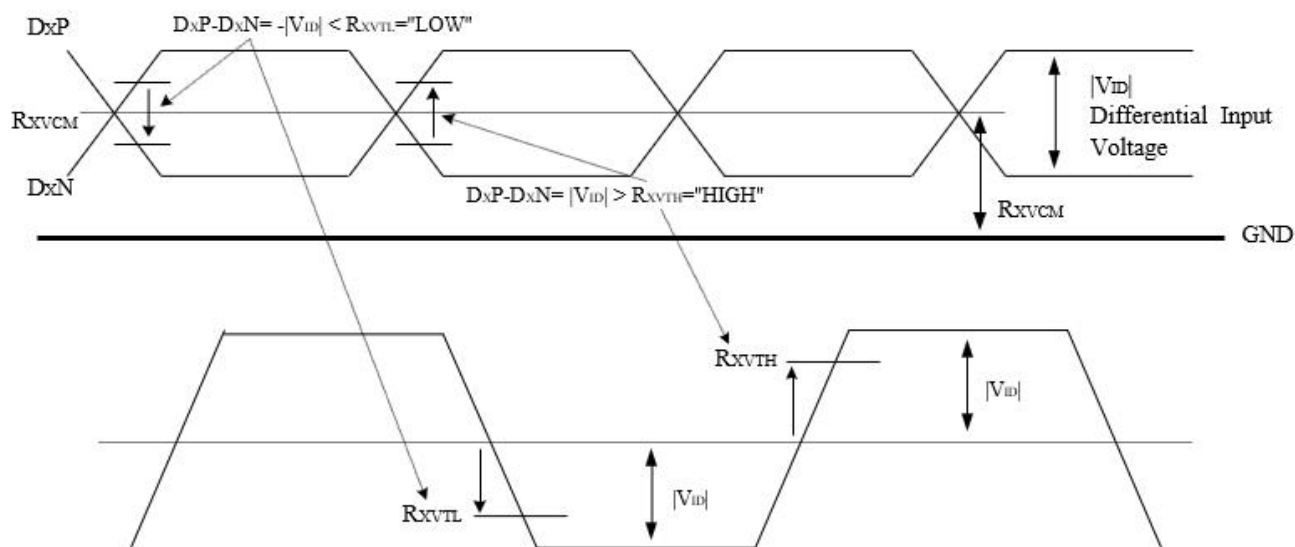
4.6 DC Specification

- LVDS Receiver Differential Input (DC Characteristics)

< Table 9-1. LVDS Rx DC Characteristics >

Parameter	Symbol	Min	Typ	Max	Unit	Notes
Differential Input High Threshold Voltage	V_{TH}	-	-	+100	mV	VCM=1.2V
Differential Input Low Threshold Voltage	V_{TL}	-100	-	-	mV	
Differential Input Common Mode Voltage	V_{CM}	1	1.2	1.4	V	
Differential Input Voltage	$ V_{ID} $	200	-	600	mV	

Single-end Signals

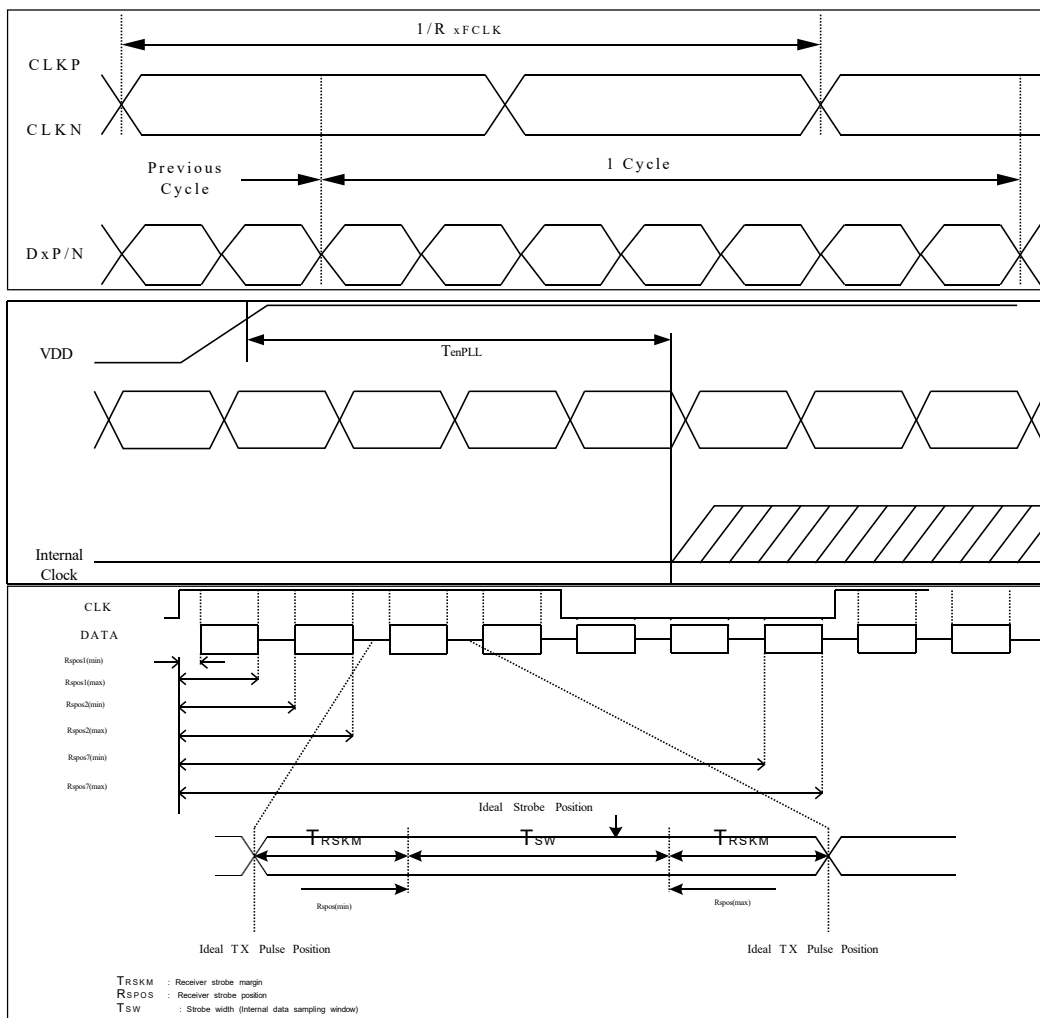


4.7 AC Specification

- LVDS Receiver Differential Input (AC Characteristics)

< Table 9-2. LVDS Rx AC Characteristics >

Parameter	Symbol	Min	Typ	Max	Unit	Notes
LVDS Strobe Width	t_{SW}	200	-	-	ps	V _{cm} =1.2V VID = 200mV @81MHz
LVDS Receiver Skew Margin	t_{RSM}	500	-	-	ps	



5.0 Interface timing Parameter

5.1 Timing Parameters (DE only mode)

For 1280RGBx800

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60Hz (LVDS)	F _{DCLK}	66.3	72.4	78.9	MHz
HSYNC period time	T _H	1380	1440	1500	DCLK
Horizontal display area	T _{HD}	1280			DCLK
HSYNC pulse width	T _{HPW}	Min.	2		
		Typ.	-		
		Max.	40		
HSYNC back porch(with pulse width)	T _{HBP}	88	88	88	DCLK
HSYNC front porch	T _{HFP}	12	72	132	DCLK
VSYNC period time	T _V	824	838	872	H
Vertical display area	T _{VD}	800			H
VSYNC pulse width	T _{VPW}	Min.	2		H
		Typ.	-		
		Max.	20		
VSYNC back porch(with pulse width)	T _{VBP}	23	23	23	H
VSYNC front porch	T _{VFP}	1	15	49	H

For 1280RGBx800 DE mode

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60Hz (LVDS)	F _{DCLK}	69.7	75	80.9	MHz
Horizontal display area	T _{HD}	1280			DCLK
HSYNC period time	T _H	1380	1440	1500	DCLK
HSYNC blanking	T _{HBP+T_{HFP}}	100	160	220	DCLK
Vertical display area	T _{VD}	800			H
VSYNC period time	T _V	842	838	872	H
VSYNC blanking	T _{VBP+T_{VFP}}	24	38	72	H

Note

1. DE Only Mode, While operation, DE signal should be have the same cycle. The input of HSYNC & VSYNC signal does not have an effect on normal operation.

2. Best operation clock frequency is 75Mhz.

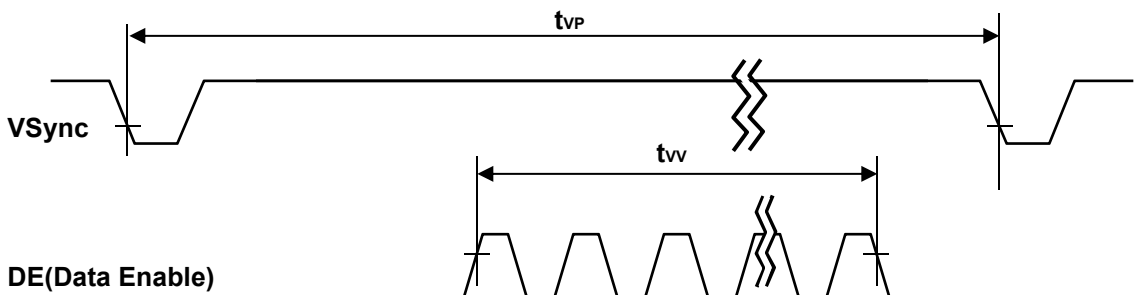
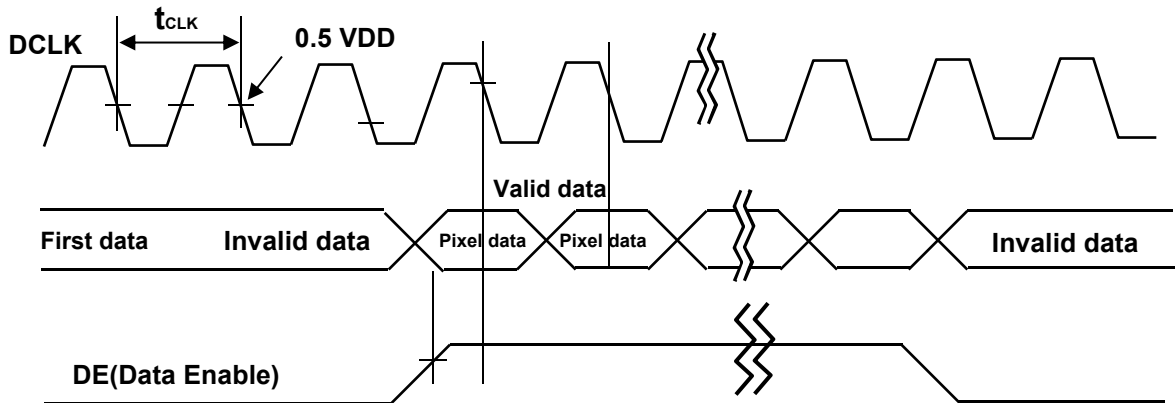
3. Frequency] = [H Total] * [V Total] * [vertical Frame rate]

H Total, V Total and Frame rate]should operate within the range between Frequency_Min and Frequency_Max

4. Except Best operation clock frequency, FOS(Flicker & Brightness & Crosstalk, Etc.) are not guaranteed.

5. Main frequency Max is 80.9Mhz MHz without spread spectrum

5.2 Signal Timing Waveform



5.3 Input Signals, Basic Display Colors and Gray Scale of Colors

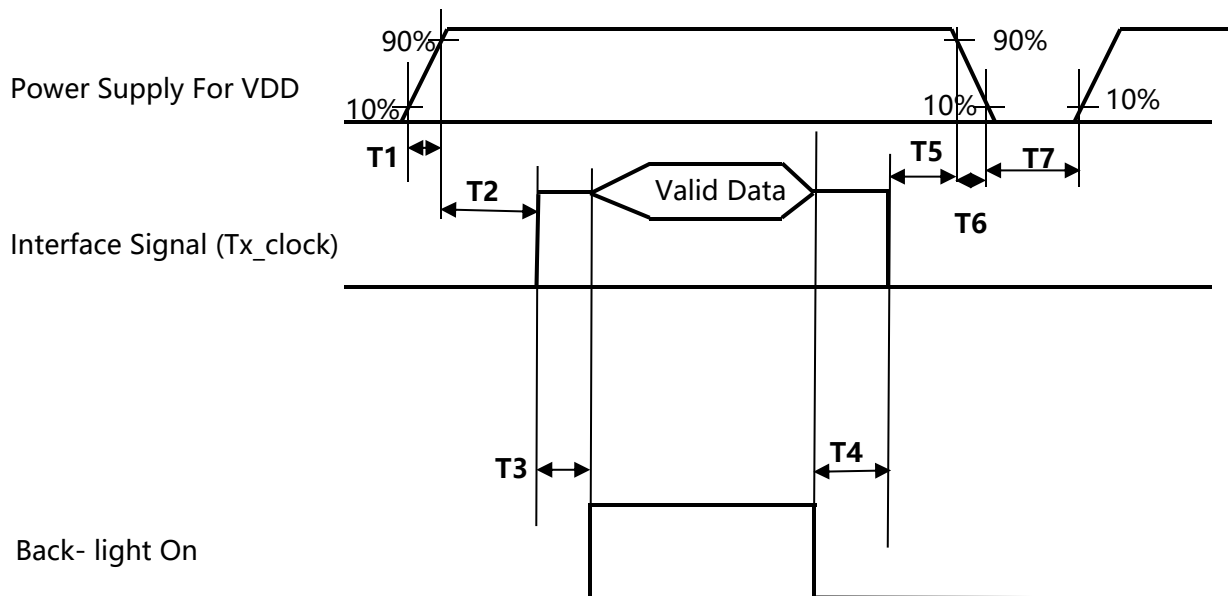
< Table 12. Input Signal and Display Color Table >

Color & Gray Scale		Input Data Signal																								
		Red Data								Green Data								Blue Data								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray Scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▽	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0
	▽	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	
Gray Scale of White	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	
	▽	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	1	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	

6.0 POWER SEQUENCE

[Ta = 25±2 °C]

To prevent a latch-up or DC operation of the Open Cell, the power on/off sequence shall be as shown in below



Parameter	Values			Units
	Min	Typ	Max	
T1	0.1	-	10	ms
T2	0.1	-	50	ms
T3	300	-	-	ms
T4	200	-	-	ms
T5	0.1	-	50	s
T6	0.1	-	10	ms
T7	500	-	-	ms

Note 1: Even though T1 is over the specified value, there is no problem if the rush current is within Spec.

Note 2: When the power supply VDD is 0V, keep the level of input signals on the low or high impedance;

※ Please avoid floating state of interface signal at invalid period.

※ When the power supply for LCD (VDD) is off, be sure to pull down the valid and invalid data to 0V.

Note 3: The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.

Note 4: T6: Voltage of VDD must decay smoothly after power-off, there should be none re-bounding voltage. (customer system decide this value)

Note 5: T7 should be measured after the Module has been fully discharged between power off and on period

7.0 OPTICAL SPECIFICATION

7.1 Overview

The test of view angle range shall be measured in a dark room (ambient luminance $\leq 1\text{lux}$ and temperature = $25\pm 2^\circ\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCON CS2000/CA310) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta\Phi=0$ ($=\theta_3$) as the 3 o'clock direction (the "right"), $\theta\Phi=90$ ($=\theta_{12}$) as the 12 o'clock direction ("upward"), $\theta\Phi=180$ ($=\theta_9$) as the 9 o'clock direction ("left") and $\theta\Phi=270$ ($=\theta_6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or Φ , the center of the measuring spot on the Display surface shall stay fixed. The luminance, color and uniformity (etc) should be tested by CS2000/CA310. The backlight should be operating for 10 minutes prior to measurement. VDD shall be $3.3 \pm 0.3\text{V}$ at 25°C . Optimum viewing angle direction is 6 'clock.

<Table 7. Optical Specifications>

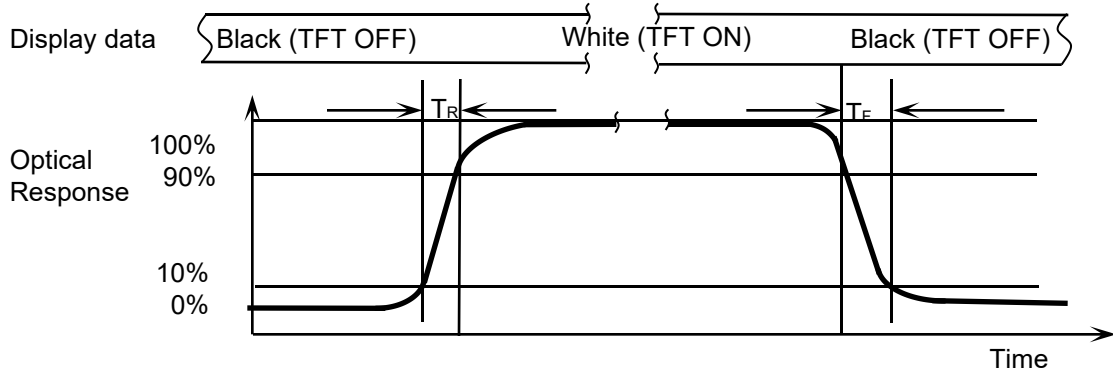
Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	Θ_3	CR > 10	80	85	-	Deg.	Note 1
		Θ_9		80	85	-	Deg.	
	Vertical	Θ_{12}		80	85	-	Deg.	
		Θ_6		80	85	-	Deg.	
Luminance Contrast ratio		CR	$\Theta = 0^\circ$	1000	1200	-		Note 2
Luminance of White	Center	Y_w	$\Theta = 0^\circ$	--	--	-	cd/m ²	Note 3
White Luminance uniformity	9 Points	ΔY_9		-	-	-	%	Note 4
Color Gamut	NTSC	CIE1931	$\Theta = 0^\circ$	68	72	-	%	Note 5
Reproduction of color	White	W_x	$\Theta = 0^\circ$	Typ -0.03	TBD	Typ +0.03		
		W_y			TBD			
Response Time		Tr+Td	Ta= 25° C $\Theta = 0^\circ$	-	30	35	ms	Note 6

Notes : 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 2).

2. Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGURE 2) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Luminance of white is defined as luminance values of center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 3 for a total of the measurements per display. The luminance is measured by CS2000/CA310 when the LED current is set at 60mA.
4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = \text{Minimum Luminance of 9 Points points} / \text{Maximum Luminance of 9 Points points}$ (See FIGURE 3).
5. The color chromaticity coordinates specified in Table 5. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
6. The electro-optical response time measurements shall be made as FIGURE 3 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_d .

Figure 4. Response Time Testing

The electro-optical response time measurements shall be made as shown in FIGURE 4 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_R and 90% to 10% is T_d .

8.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

<Table 9. Reliability test>

Test Item	BOE 12.1 WXGA FOB	ODM review	Remark
High Temperature Storage	+70°C, 240h	+70°C, 240h	
Low Temperature Storage	-20°C, 240h	-20°C, 240h	
High Temperature Operation	+60°C, 240h	+60°C, 240h	
Low Temperature Operation	-10°C, 240h	-10°C, 240h	
High Temperature & Humidity Operation Test	50°C, 90%RH ,240h	50°C, 90%RH ,240h	
Temperature Shock Test storage	-30°C~70°C, 1hr/cycle, 100cycle	-30°C~70°C, 1hr/cycle, 100cycle	
Image Sticking	Chess 10*10 Pattern 1hrs Room temperature Gray 127 no image sticking	Chess 10*10 Pattern 1hrs Room temperature Gray 127 no image sticking	
ESD	150pF+330Ω, Air : +/- 15kV, Contact : +/- 8kV , Class B	150pF+330Ω, Air : +/- 15kV, Contact : +/- 8kV , Class B	
Packing VIB	1.47G, 1-200hz, X, Y, ±Z, 30min/Axis	-	

Note : After the reliability test, the product only guarantee function normally without any fatal defect (non-display, line defect, abnormal display etc). All the cosmetic specification is judged before the reliability test.

9.0 Precautions

Please pay attention to the followings when you use this TFT LCD Panel.

9.1 Mounting Precautions

- (1) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (2) You must mount a module using specified mounting holes (Details refer to the drawings).
- (3) Please make sure to avoid external forces applied to the Source PCB or FPC and D-IC during the process of handling or assembling. If not, It causes panel damage or malfunction.
- (4) Note that polarizers are very fragile and could be easily damaged. Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
- (5) Do not pull or fold the source D-IC which connect the source PCB or FPC and the panel.
- Do not pull or fold the LED wire.
- (6) After removing the protective film, when the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with alcohol or purified water.
- Do not strong polar solvent because they cause chemical damage to the polarizer.
- (7) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (8) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (9) Since the LCD is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it falls from a high place or receives a strong shock, the glass may be broken.
- (10) Do not disassemble the module.
- (11) To determine the optimum mounting angle, refer to the viewing angle range in the specification for each model.
- (12) If the customer's set presses the main parts of the LCD, the LCD may show the abnormal display. But this phenomenon does not mean the malfunction of the LCD and should be pressed by the way of mutual agreement.
- (13) Do not drop water or any chemicals onto the LCD's surface.

9.2 Operating Precautions

- (1) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (2) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (3) The electrochemical reaction caused by DC voltage will lead to LCD degradation, so DC drive should be avoided.
- (4) The LCD modules use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.
- (5) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- (6) Design the length of cable to connect between the connector for back-light and the converter as short as possible and the shorter cable shall be connected directly.
The longer cable between that of back-light and that of converter may cause the luminance of LED to lower and need a higher startup voltage(Vs).
- (7) Connectors are precise devices for connecting PCB and transmitting electrical signals. Operators should insert and unplug MDL in parallel when assembling MDL.
- (8) Do not connect or disconnect the cable to/ from the module at the "Power On" condition.
- (9) When the module is operating, do not lose CLK, ENAB signals. If any one these signals is lost, the LCD panel would be damaged.
- (10) Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (11) Do not re-adjust variable resistor or switch etc.
- (12) Long Side LED Bar design is recommended when using E-LED type Back Light.

9.3 Electrostatic Discharge Control

- (1) Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly. Keep products as far away from static electricity as possible.
- (2) Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

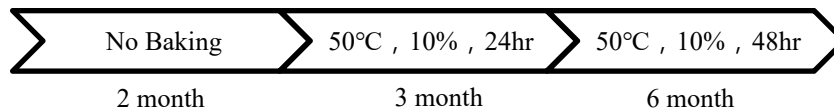
9.4 Precautions for Strong Light Exposure

It is not allowed to store or run directly in strong light or in high temperature and humidity for a long time; Strong light exposure causes degradation of polarizer and color filter.

9.5 Storage Precautions

When storing modules as spares for a long time, the following precautions are necessary.

- (1) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.
Temperature : 5 ~ 40 °C
- (2) Humidity : 35 ~ 75 %RH
- (3) Period : 6 months
- (4) Control of ventilation and temperature is necessary.
- (5) Please make sure to protect the product from strong light exposure, water or moisture.
Be careful for condensation.
- (6) Store in a polyethylene bag with sealed so as not to enter fresh air outside in it.
- (7) Do not store the LCD near organic solvents or corrosive gasses.
- (8) Please keep the Modules/OC/FOG at a circumstance shown below Fig.



9.6 Precautions for Protection Film

- (1) Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, If possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
- (2) In handling the LCD, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary.

9.7 Appropriate Condition for Display

- (1) Normal operating condition
 - Temperature: 0 ~ 40°C
 - Operating Ambient Humidity : 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)
 - Suitable operating time: under XX hours a day. (Please contract BOE in advance for 7*24hrs or more than suggested Operating time)
 - Long-term lighting products recommended regular shutdown

- (2) Special operating condition

If the product will be used in extreme conditions such as high temperature, humidity, display patterns or 7*24hrs operation time etc., It is strongly recommended to contact BOE for Application engineering advice. Otherwise, its reliability and function may not be guaranteed.

- (3) Black image or moving image is strongly recommended as a screen save.

- (4) Lifetime in this spec. is guaranteed only when commercial display is used according to operating usages.
- (5) Please contact BOE in advance when you want to switch between portrait and landscape screen
- (6) Please contact BOE in advance for outdoor operation.
- (7) Please contact BOE in advance when you display the same pattern for a long time.
- (8) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen. To avoid image sticking, it is recommended to use a screen saver.
- (9) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- (10) Dew drop atmosphere should be avoided.
- (11) The storage room should be equipped with a good ventilation facility and avoid to expose to corrosive gas , which has a temperature controlling system.
- (12) When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD's surface which may affect the operation of the polarizer and the LCD.
- (13) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD may turn black at temperature above its operational range. However those phenomena do not mean malfunction or out of order with the LCD. The LCD will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

9.8 Cleaning at the end user customer environment

- IPA (Isopropyl Alcohol) wipes, Lysol wipes, Clorox wipes, and other general Hospital disinfectants (on a cloth, not sprayed directly on equipment) which may include ingredients such as IPA, Ammonium Chloride, or Accelerated Hydrogen Peroxide can be used on the LCD glass surface.

9.9 Others

A. LC Leak

- If the liquid crystal material leaks from the panel, it is recommended to wash the LC with acetone or ethanol and then burn it.
- In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.

B. Rework

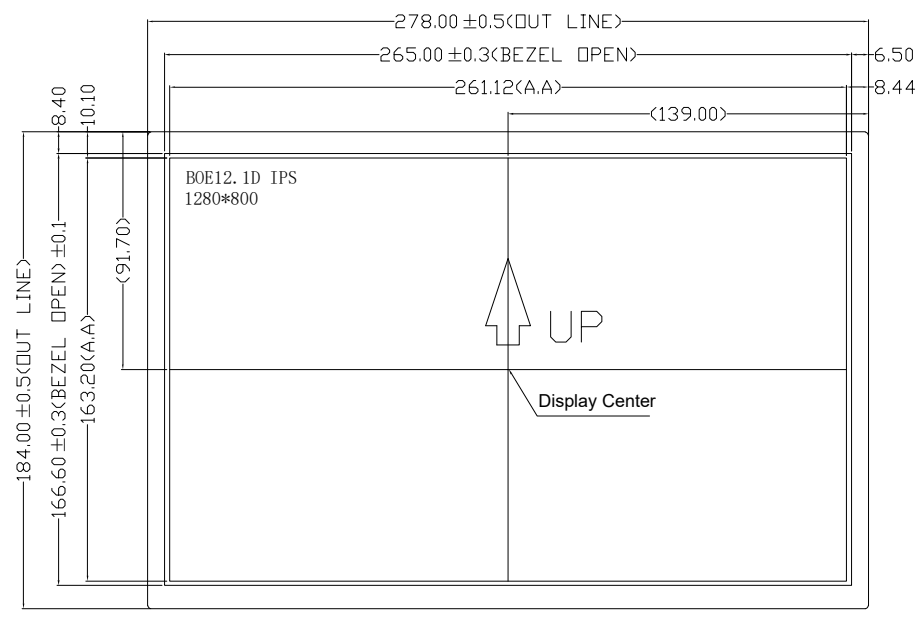
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

9.0 PACKING INFORMATION

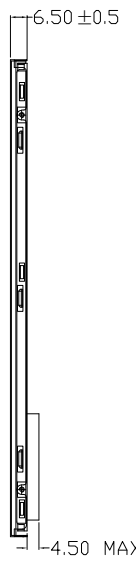
TBD

10.Mechanical Drawing

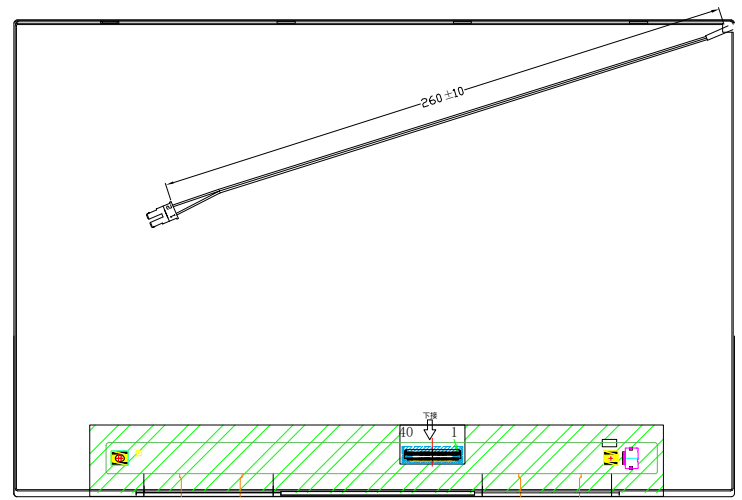
Front View



Side View

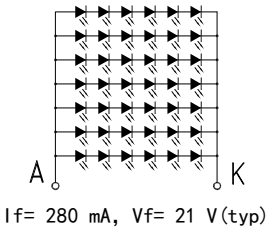


Back View



1	NC
2-4	VDD
5	NC
6-7	GND
8	RINO-
9	RINO+
10	GND
11	RIN1-
12	RIN1+
13	GND
14	RIN2-
15	RIN2+
16	GND
17	LVDS_CLK-
18	LVDS_CLK+
19	GND
20	RIN3-
21	RIN3+
22	GND
23-24	NC
25	GND
26	SDA
27	SCL
28	GND
29-30	NC
31-33	GND
34	NC
35	PWM
36	LED_EN
37	NC
38-40	VLED

- Notes:
1. Display : 12.1", TFT
 2. Resolution: 1280xRGBx800
 3. LCD Viewing Direction: ALL,
 4. Display Mode: Normally Black/Transmissive
 5. LCM Brightness: 350cd/m² (TYP), uniformity ≥70%
 6. unmark Tolerance: ±0.5
 7. OPERATING TEMP: -10° C ~ +60° C
 8. STORAGE TEMP: -20° C ~ +70° C
 9. Requirements on Environmental Protection: ROHS



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