

**PRODUCT SPECIFICATION****TFT-LCD MODULE****Module No: 100294-01**☒ Preliminary Specification☐ Approval Specification

Designed by	Checked by	Approved by

Final Approval by Customer

Approved by	Comment

For more information, contact us:

WWW.DCLTEK.COM | 1-800-407-8917 | SALES@DCLTEK.COM

※The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

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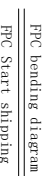
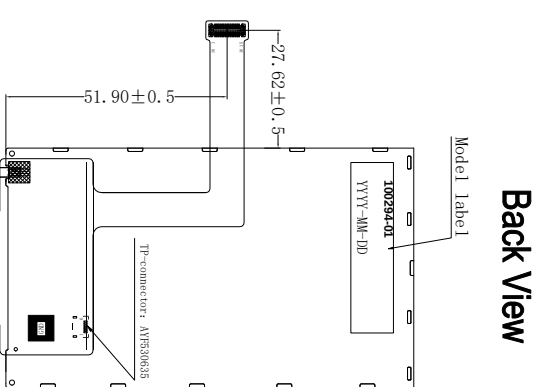
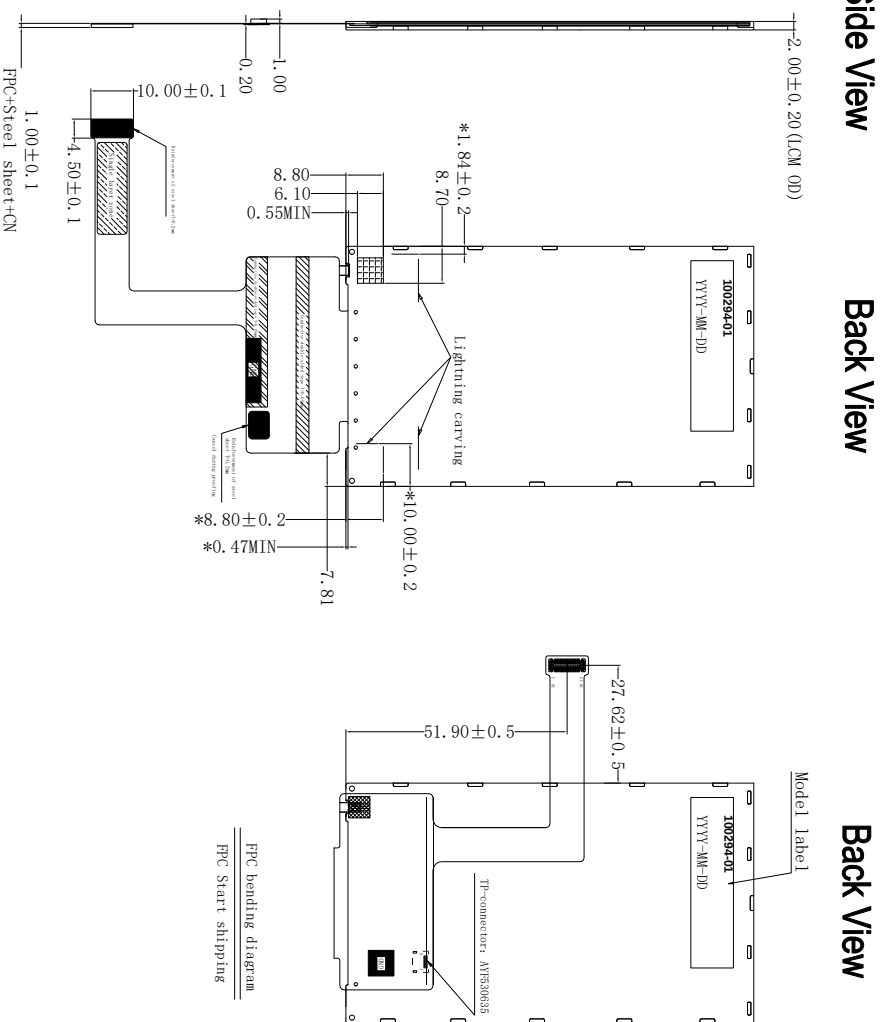
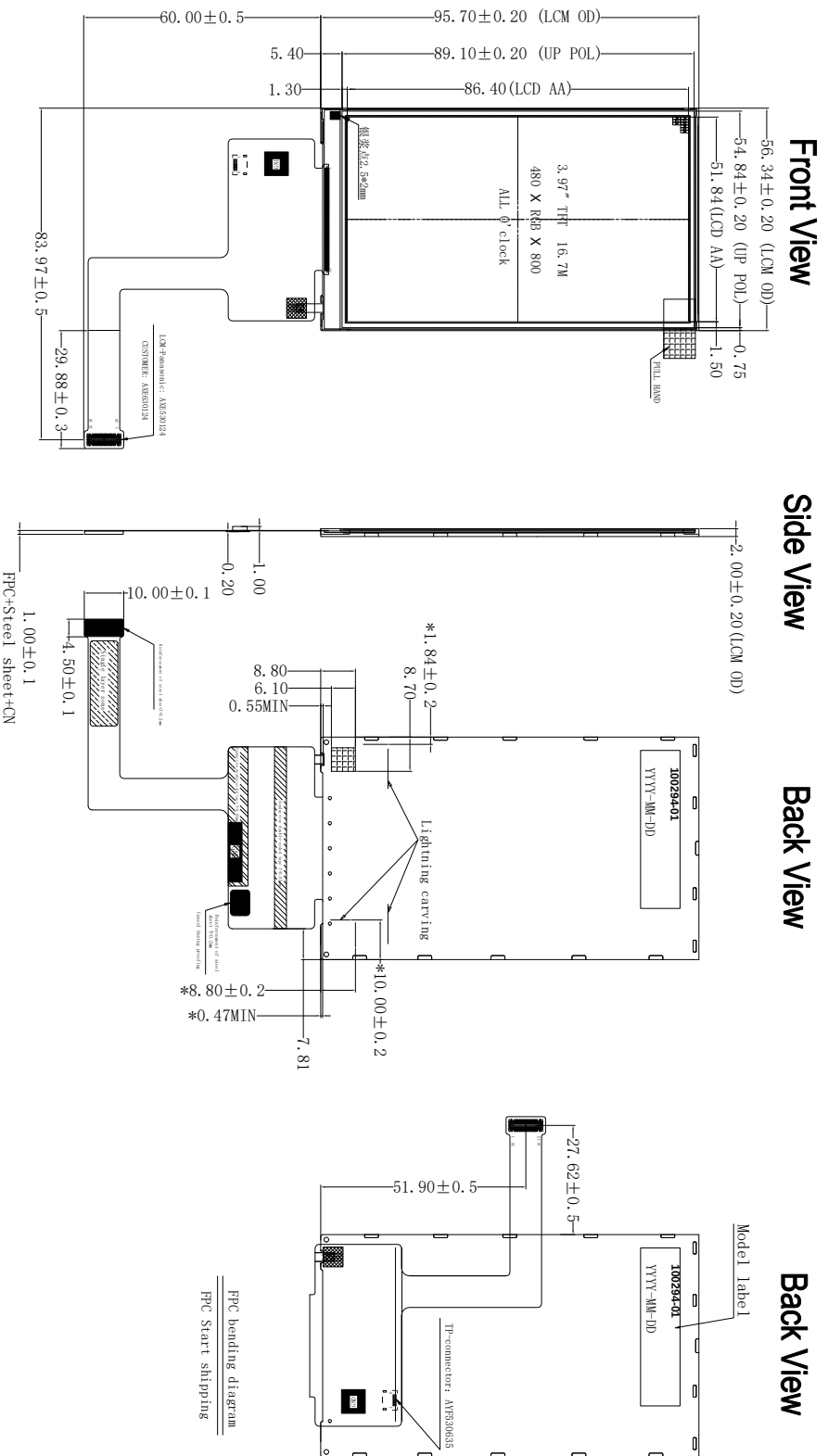
1.Document Revision History

Version	Contents	Date	Note
V0	NEW	2023.12.07	

2. General Description

N0	Item	Specification	Unit	Remark
1	LCD Size	TFT"3.97	inch	-
2	Panel Type	IPS	-	-
3	Number of pixels	480 x RGB x 800	pixel	-
4	Display mode	Normally Black	-	-
5	Display colors	16.7M	-	-
6	Viewing Direction	ALL	-	-
7	Contrast Ratio	800	-	
8	Luminance	300	nit	
9	Module Size	56.34 (W) x95.70(L) x2.0(T)	mm	Note
10	Panel Active Area	51.84 (W) x 86.40 (L)	mm	Note
11	Pixel Pitch	0.108(H) × 0.108(V)	mm	-
12	Weight	TBD	g	-
13	Driver IC	ST7701S-G5	bit	-
14	Light Source	8 white LED	-	-
15	Interface	MIPI	-	-
16	Operating Temperature	-20~+70	°C	-
17	Storage Temperature	-30~+80	°C	-

3.EXTERNAL DIMENSIONS



NO	PIN NAME
1	GND
2	MTP PWR
3	10VCC
4	VC1
5	TE
6	GND
7	DS1 D1P
8	DS1 D1N
9	GND
10	DS1 CLAMP
11	DS1 CLAN
12	GND
13	DS1 D0P
14	DS1 D0N
15	GND
16	GND
17	NC
18	LED K
19	LED R
20	LED A
21	LED A
22	NC
23	GND
24	NC
25	BRESET (1)
26	CTP RESET (2)
27	CTP SCL (3)
28	CTP SDA (4)
29	CTP INT (5)
30	CTP 2.8V (6)

CTP		23	GND
		24	NC
1	GND	25	RESET (1)
2	CTP /MST	26	CTP RESET (2)
3	CTP SCL2.8V	27	CTP SCL (3)
4	CTP SDA2.8V	28	CTP SDA (4)
5	CTP INT	29	CTP INT (5)
6	CTP 2.8V	30	CTP 2.8V (6)

TFT Notes:

1. Display : 3.97"BOE, TFT IPS
2. Resolution: 480xRGBx800
3. LCD Viewing Direction:ALL
4. DisplayMode: Normally Black
5. LCD Driver: ST7701S-G5
6. OPERATING VOLTAGE:VDD 2.8~3.3V
7. LCM Luminance: 350cd/m²(Typ)
8. *:Critical dimension : () Reference dimension;
Unmarked tolerance : $\pm 0.2\text{mm}$
9. OPERATING TEMP: -20 $^{\circ}\text{C}$ ~ +70 $^{\circ}\text{C}$
10. STORAGE TEMP: -30 $^{\circ}\text{C}$ ~ +80 $^{\circ}\text{C}$
11. Requirements on Environmental Protection: ROHS

(8LED)

[illegible] $IF=20\text{mA}, VF=24\pm 2.4\text{V}$

△	NEW	2023. 11. 23
REV	DESCRIPTION	DATE
		NAME

SCALE: 1/1	UNIT: mm	PAGE: 1/1			Approve	Check	Drawn
Part No:	100294-01		VER: VO				
Customer No:							

DCU  **TEK**

4. Interface Specification

NO.	Symbol	Function	Remark
1	GND	Power Ground	
2	MTP_PWM(NC)	NC	
3	IOVCC 1.8V	I/O Power supply : 1.65V ~ 3.3V	
4	VCI 2.8V	Power supply : 2.6V ~ 3.6V	
5	TE	Tearing Effect Output Pin TE	
6	GND	Power Ground	
7	MIPI_1P	MIPI-DSI clock data Lane 1 pegative-end input/output pin.	
8	MIPI_1N	MIPI-DSI clock data Lane 1 nositive-end input/output pin.	
9	GND	Power Ground	
10	MIPI_CKP	MIPI-DSI clock data Lane pegative-end input pin.	
11	MIPI_CKN	MIPI-DSI clock data Lane nositive-end input pin.	
12	GND	Power Ground	
13	MIPI_0P	MIPI-DSI clock data Lane 1 pegative-end input/output pin.	
14	MIPI_0N	MIPI-DSI clock data Lane 1 nositive-end input/output pin.	
15	GND	Power Ground	
16	GND	Power Ground	
17	NC	NC	
18	LEDK	Power supply for backlight cathode input terminal.	
19	LEDK	Power supply for backlight cathode input terminal.	
20	LEDA	Power supply for backlight anode input terminal.	

21	LEDA	Power supply for backlight anode input terminal.	
22	NC	NC	
23	GND	Power Ground	
24	NC	NC	
25	RESET	Reset signal input terminal. Active at 'L'.	
26	CTP_RST	Touch panel reset	
27	CTP_SCL	Touch panel I2C clock	
28	CTP_SDA	Touch panel I2C data	
29	CTP_INT	Touch panel interrupt output	
30	CTP_VCC	Touch panel Power supply 2.8~3.3V	

5. Electrical Characteristics

5.1 TFT DC Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage for I/O	IOVCC	1.65	1.8	3.3	V
Supply Voltage for(DC/DC)	VCC	2.6	2.8	3.6	V

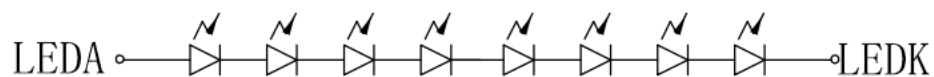
5.2 LED Backlight Specification

The back-light system is an edge-lighting type with 8 white LEDs. The characteristics of the back-light are shown in the following tables.

Item	Symbol	Min	Typ	Max	Unit	Notes
Forward voltage	Vf	21.6	24.0	26.4	V	--
Forward current	IF	--	20	--	mA	--
Luminance(With LCD)	Lv	--	300	-	cd/m ²	--
LED life time	N/A	--	30,000	--	Hr	Note 1

Note:(1) The "LED life time" is defined as the module brightness decrease to 50% of original brightness at IL=20mA/LED. The LED life time could be decreased if operating IL is larger than 20mA/LED.

LED circuit:



(8LED) IF=20mA, VF=24 ± 2.4V

6. Timing Characteristics

Timing POWER ON/OFF SEQUENCE

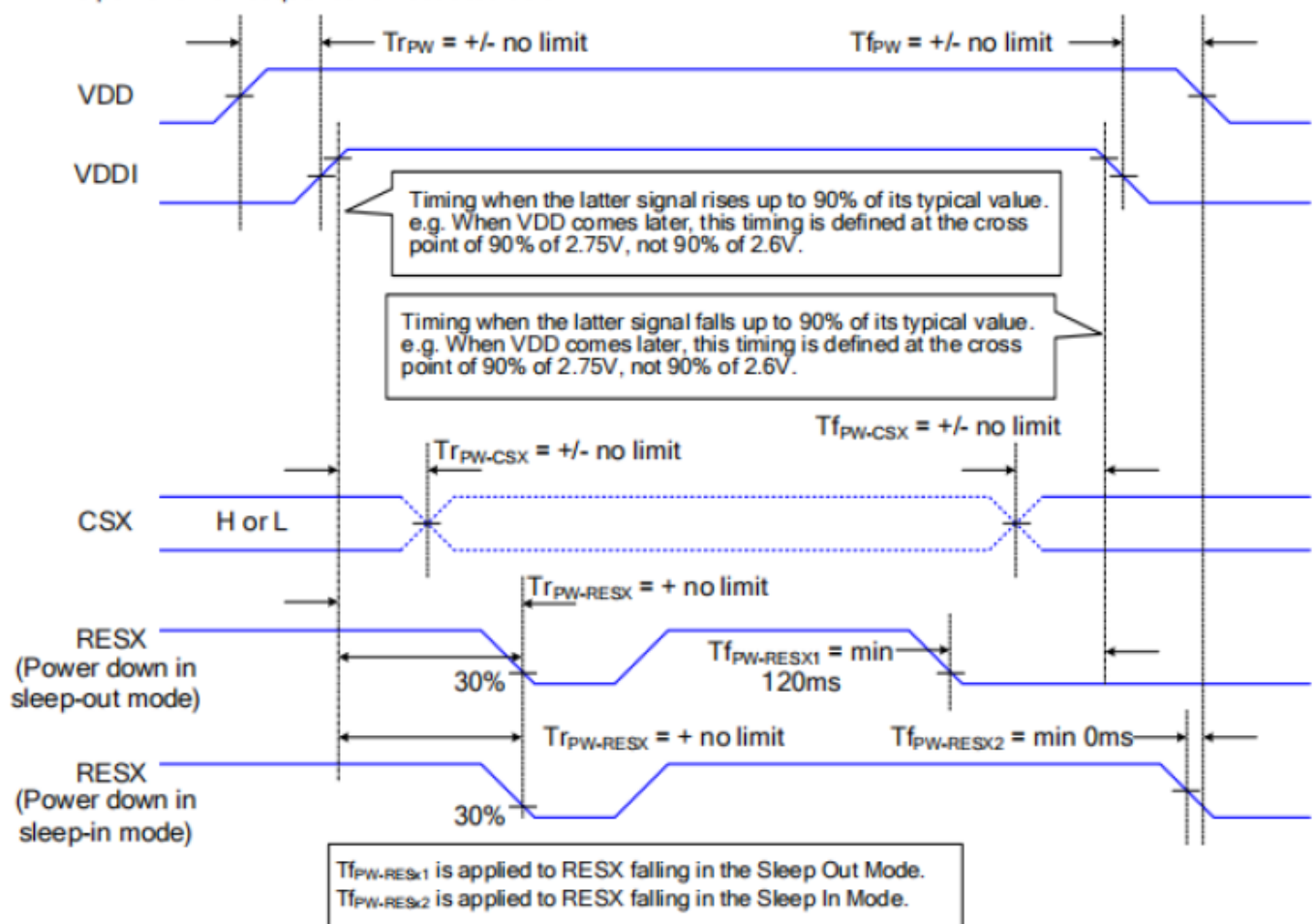
VDDI and VDDA can be applied or powered down in any order. During the Power Off sequence, if the LCD is in the Sleep Out mode, VDDA and VDDI must be powered down with minimum 120msec. If the LCD is in the Sleep In mode, VDDA and VDDI can be powered down with minimum 0msec after the RESX is released.

CSX can be applied at any timing or can be permanently grounded. RESX has high priority over CSX.

Notes:

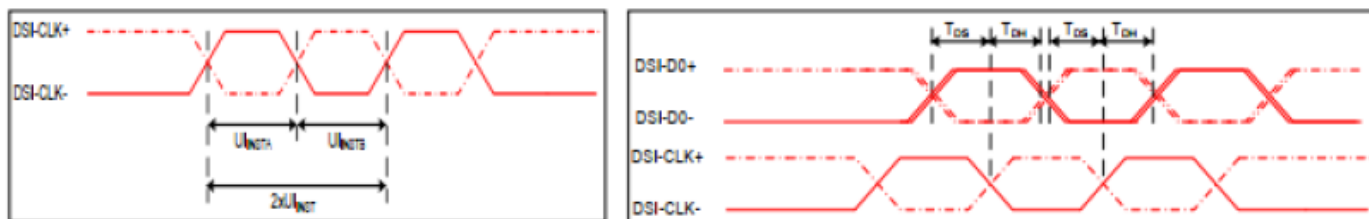
1. There will be no damage to the ST7701S if the power sequences are not met.
2. There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.
3. There will be no abnormal visible effects on the display between the end of Power On Sequence and before receiving the Sleep Out command, and also between receiving the Sleep In command and the Power Off Sequence.
4. If the RESX line is not steadily held by the host during the Power On Sequence as defined in Sections 9.1 and 9.2, then it will be necessary to apply the Hardware Reset (RESX) after the completion of the Host Power On Sequence to ensure correct operations. Otherwise, all the functions are not guaranteed.

The power on/off sequence is illustrated below



DSI-MIPI Interface Timing Characteristics of IC

High Speed Mode



DSI clock channel timing

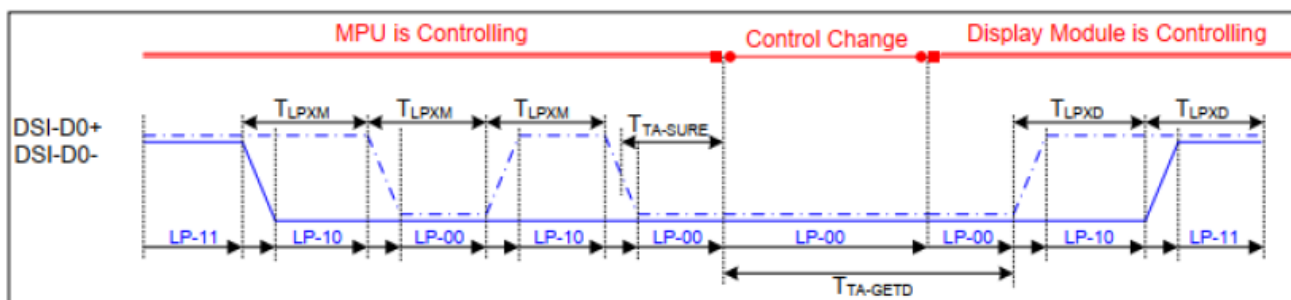
Rising and falling time on clock and data channel

$V_{DDI}=1.8, V_{DD}=2.8, A_{GND}=D_{GND}=0V, T_a=25^{\circ}C$

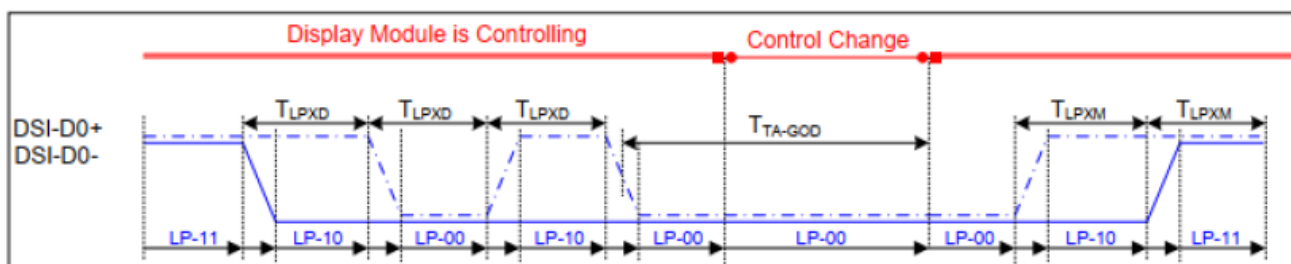
Signal	Symbol	Parameter	MIN	MAX	Unit	Description
DSI-CLK+/-	$2xUI_{INSTA}$	Double UI instantaneous	4	25	ns	
DSI-CLK+/-	UI_{INSTA} UI_{INSTB}	UI instantaneous halves	2	12.5	ns	$UI = UI_{INSTA} = UI_{INSTB}$
DSI-Dn+/-	tDS	Data to clock setup time	0.15	-	UI	
DSI-Dn+/-	tDH	Data to clock hold time	0.15	-	UI	

Mipi Interface- High Speed Mode Timing Characteristics

Low Power Mode



Bus Turnaround (BTA) from display module to MPU Timing



Bus Turnaround (BTA) from MPU to display module Timing

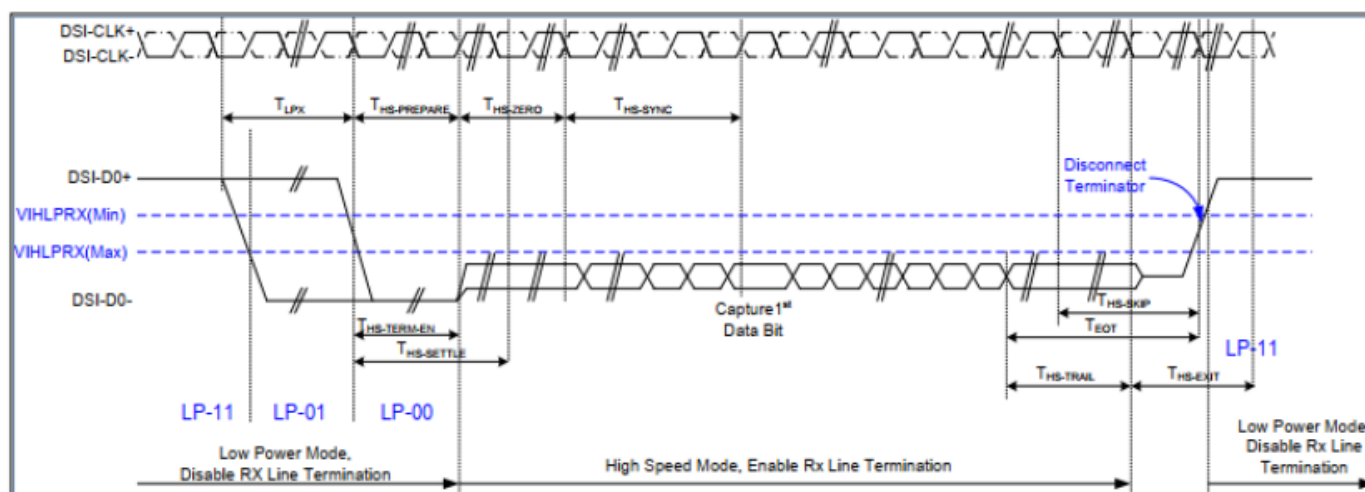
$VDDI=1.8, VDD=2.8, AGND=DGND=0V, T_a=25\text{ }^{\circ}\text{C}$

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
DSI-D0+/-	TLPXM	Length of LP-00,LP-01, LP-10 or LP-11 periods MPU→Display Module	50	75	ns	Input
DSI-D0+/-	TLPXD	Length of LP-00,LP-01, LP-10 or LP-11 periods MPU→Display Module	50	75	ns	Output
DSI-D0+/-	TTA-SURED	Time-out before the MPU start driving	T_{LPXD}	$2 \times T_{LPXD}$	ns	Output
DSI-D0+/-	TTA-GETD	Time to drive LP-00 by display module	$5 \times T_{LPXD}$		ns	Input
DSI-D0+/-	TTA-GOD	Time to drive LP-00 after turnaround request-MPU	$4 \times T_{LPXD}$		ns	Output

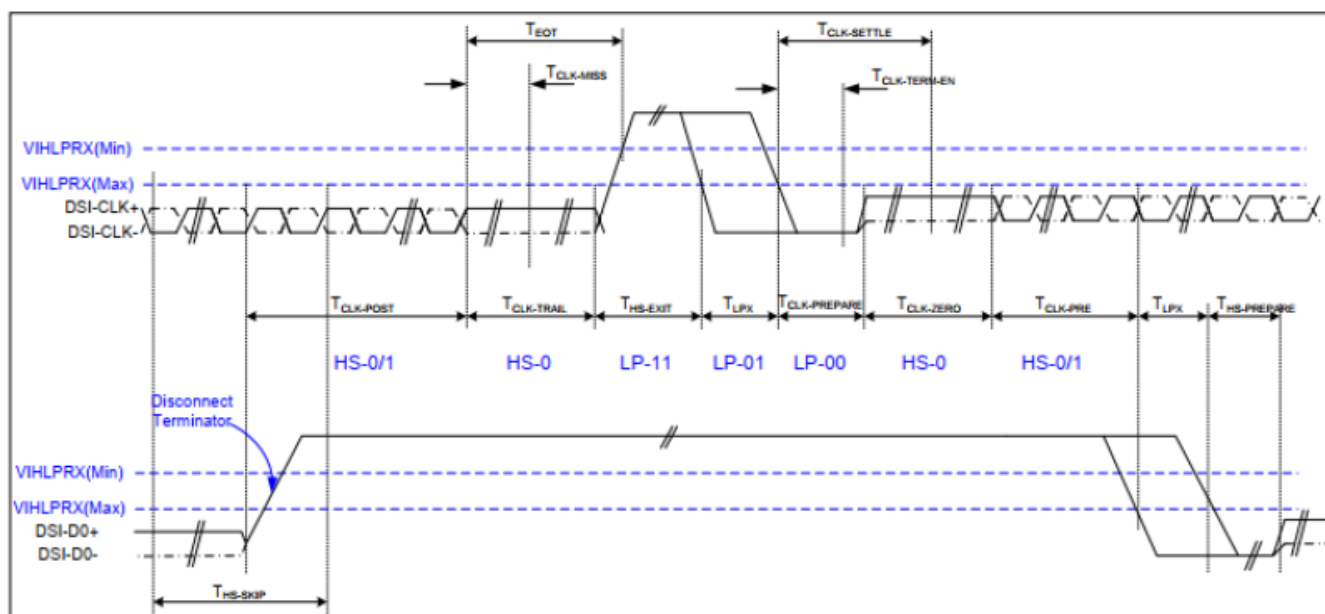
Mipi Interface Low Power Mode Timing Characteristics

DSI Bursts Mode

DSI Bursts Mode



Data lanes-Low Power Mode to/from High Speed Mode Timing



Clock lanes- High Speed Mode to/from Low Power Mode Timing

VDDI=1.8, VDD=2.8, AGND=DGND=0V, Ta=25 °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
Low Power Mode to High Speed Mode Timing						
DSI-Dn+/-	TLPX	Length of any low power state period	50	-	ns	Input
DSI-Dn+/-	THS-PREPARE	Time to drive LP-00 to prepare for HS transmission	40+4 UI	85+6 UI	ns	Input
DSI-Dn+/-	THS-TERM-EN	Time to enable data receiver line termination measured from when Dn crosses VILMAX	-	35+4 UI	ns	Input
DSI-Dn+/-	THS-PREPARE + THS-ZERO	THS-PREPARE + time to drive HS-0 before the sync sequence	140+ 10UI	-	ns	Input
High Speed Mode to Low Power Mode Timing						
DSI-Dn+/-	THS-SKIP	Time-out at display module to ignore transition period of EoT	40	55+4 UI	ns	Input
DSI-Dn+/-	THS-EXIT	Time to drive LP-11 after HS burst	100	-	ns	Input
DSI-Dn+/-	THS-TRAIL	Time to drive flipped differential state after last payload data bit of a HS transmission burst	60+4 UI	-	ns	Input
High Speed Mode to/from Low Power Mode Timing						
DSI-CLK+/-	TCLK-POS	Time that the MPU shall continue sending HS clock after the last associated data lane has transition to LP mode	60+5 2UI	-	ns	Input
DSI-CLK+/-	TCLK-TRAIL	Time to drive HS differential state after last payload clock bit of a HS transmission burst	60	-	ns	Input
DSI-CLK+/-	THS-EXIT	Time to drive LP-11 after HS burst	100	-	ns	Input
DSI-CLK+/-	TCLK-PREPARE	Time to drive LP-00 to prepare for HS transmission	38	95	ns	Input
DSI-CLK+/-	TCLK-TERM-EN	Time-out at clock lan display module to enable HS transmission	-	38	ns	Input
DSI-CLK+/-	TCLK-PREPARE + TCLK-ZERO	Minimum lead HS-0 drive period before starting clock	300	-	ns	Input
DSI-CLK+/-	TCLK-PRE	Time that the HS clock shall be driven prior to any associated data lane beginning the transition from LP to HS mode	8UI	-	ns	Input
DSI-CLK+/-	TEOT	Time form start of TCLK-TRAIL period to start of LP-11 state	-	105n s+12 UI	ns	Input

Reset Description:

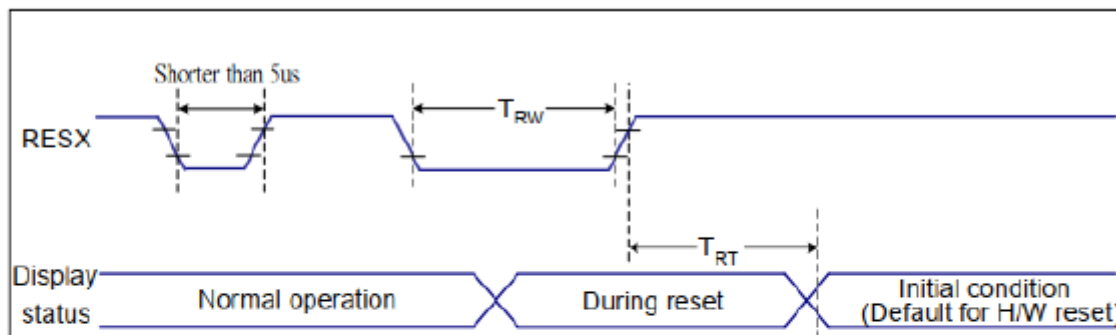


Figure 9 Reset Timing

VDDI=1.8, VDD=2.8, AGND=DGND=0V, $T_a=25^\circ\text{C}$

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120 (Note 1, 6, 7)	ms

Table 9 Reset Timing

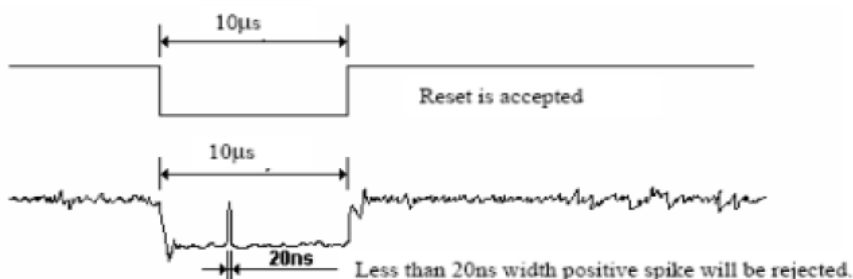
Notes:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (t_{RT}) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.

4. Spike Rejection also applies during a valid reset pulse as shown below:



5. When Reset applied during Sleep In Mode.
6. When Reset applied during Sleep Out Mode.
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

7. Optical Specification

7.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25 \pm 2^\circ\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta\phi=0$ ($=\theta_3$) as the 3 o'clock direction (the "right"), $\theta\phi=90$ ($=\theta_{12}$) as the 12 o'clock direction ("upward"), $\theta\phi=180$ ($=\theta_9$) as the 9 o'clock direction ("left") and $\theta\phi=270$ ($=\theta_6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or ϕ , the center of the measuring spot on the Display surface shall stay fixed. Optimum viewing angle direction is 6 "clock.

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	Θ_3	CR > 10	80	85	-	Deg.	Note 1
		Θ_9		80	85	-	Deg.	
	Vertical	Θ_{12}		80	85	-	Deg.	
		Θ_6		80	85	-	Deg.	
Contrast ratio		CR	$\Theta = 0^\circ$	550	800	-		Note 2
Transmittance		Tr.		3.34	3.93	-	%	Without APF Note 3
White Chromaticity		x_w		0.277	0.292	0.307		Note 4 CF Glass Base on C Light
		y_w		0.318	0.333	0.348		
Reproduction of color (C light)	Red	R_x		0.650	0.665	0.680		
		R_y		0.308	0.323	0.338		
	Green	G_x		0.257	0.272	0.287		
		G_y		0.573	0.588	0.613		
	Blue	B_x		0.119	0.134	0.149		
		B_y		0.106	0.121	0.136		
Response Time (Rising + Falling)		$T_r + T_f$	Ta= 25° C $\Theta = 0^\circ$	-	35	-	ms	Note 5

Note:

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).
2. Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Transmittance is the Value with Polarizer
4. The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
5. The electro-optical response time measurements shall be made as FIGURE 3 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_d .

Figure 1. The Definition of V_{th} & V_{sat}

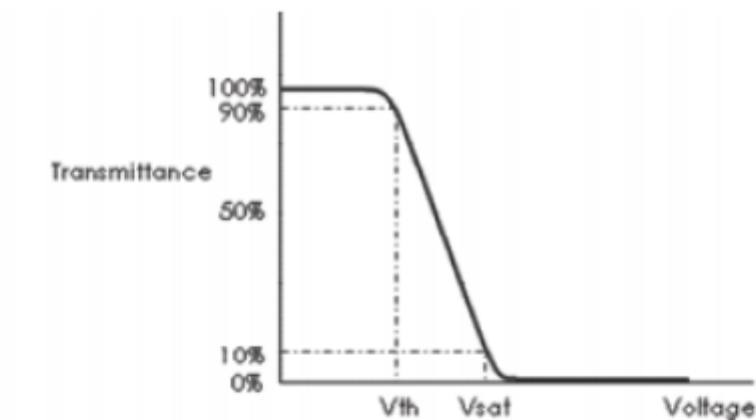


Figure 2. Measurement Set Up

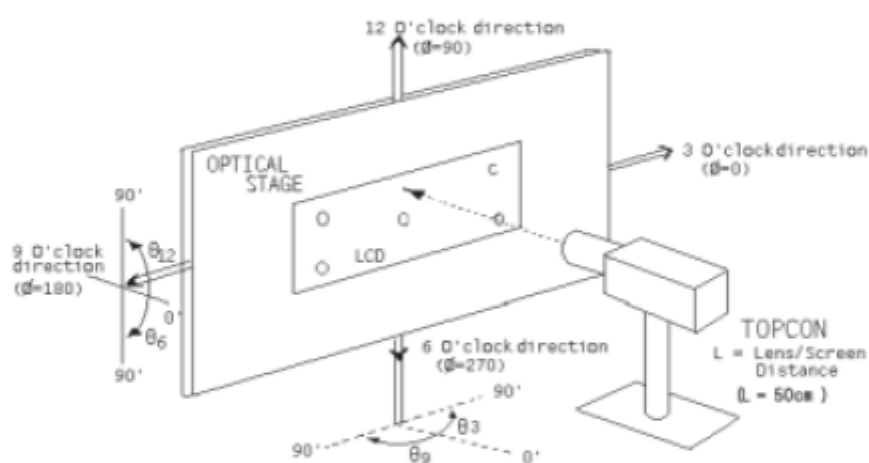
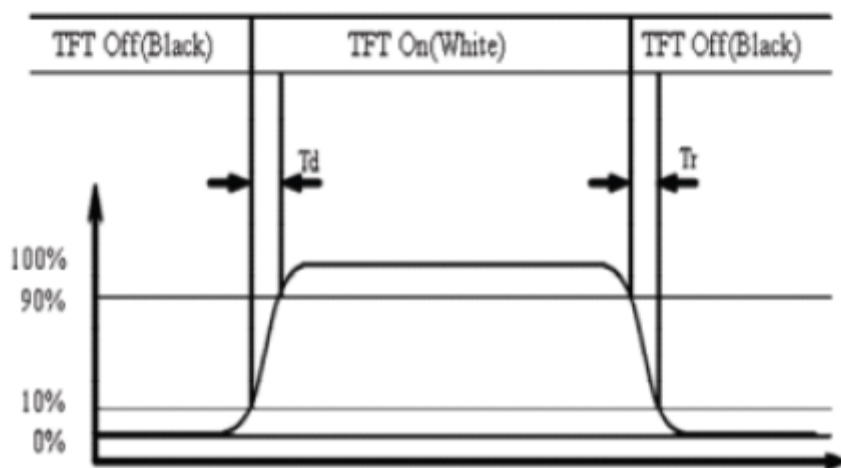


Figure 3. Response Time Testing



8. Reliability Test Items

Item	Test Condition	Criterion
High Temperature Operation	70 °C, 48 hrs	Note1, Note2
Low temperature operation	-20 °C, 48 hrs	
High Temperature Storage	80 °C, 48 hrs	
Low Temperature Storage	-30 °C, 48 hrs	
High Temp. & High Humidity Storage	40 °C, 80% RH, 48hrs	
Thermal Shock (Static)	-20°C, 30 min /60°C, 30 min, 20 cycles	

Note1: Evaluation should be tested after storage at room temperature for two hours.

Note2:

Pass: Normal display image no line defect.

Fail: No display image, or line defects.

Partial transformation of the module parts should be ignored.

9. Precautions

Please pay attentions to the followings as using the LCD module.

Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.

(e) Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean the display surface. It might damage the touch panel surface permanently. The recommended solvents are water and Isopropyl alcohol.

(f) Wipe off water droplets or oil immediately.

(g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.

(h) Do not touch the output pins directly with bare hands.

(i) Do not disassemble the LCD module.

(j) Do not lift the FPC of Touch Panel.

Storage

(a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.

(b) Do not expose the LCD modules to sunlight directly.

(c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.

(d) Avoid condensation of water. It may cause improper operation.

(e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

Operation (a) When mounting or dismounting the LCD modules, turn the power off.

(b) Protect the LCD modules from electric shock.

(c) The Driver IC control algorithms stated above should always be obeyed to avoid damaging the LSI and electronic circuit.

(d) Be careful to avoid mixing up the polarity of power supply for backlight.

(e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.

(f) When a static image is displayed for a long time, remnant image is likely to occur.

(g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.

(h) Most of the touch screens have air vent to equalize the inside air pressure to the outside one. The air vent must

be open and liquid contact must be avoided as the liquid may be absorbed if the liquid is accumulated near the air vent.

(i) For the fragility of ITO film, it should avoid to use too tapering pen as the input material.

(a) If a cushion is used between bezel/housing and film must be choose as free as enough to absorb the expansion and contraction to avoid the distortion of film.

(b) The cushion must be placed out of the Viewing Area.

(c) Bezel/Housing edge must be posited between Key Area and Viewing Area. The edge enters the Key Area may cause unexpected input if the gap is too narrow or foreign particles like dusts exist between Bezel/Housing and ITO film.

(d) Mounting example:

The corner part has conductivity. Do not touch any metal part after mounting.

Others

a) If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.

b) For the fragility of polarizer, it is recommended to attach a transparent protective plate over the display surface.

c) It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.

10.HSF Requirements

☒ RoHS(Restriction of the use of certain Hazardous Substances)

☐ HF (Halogen Free)

☐ REACH (Regulation the Registration, Evaluation, Authorization and Restriction of Chemicals)

☐ Other regulations

11.Packaging diagram

TBD

深圳市丽台电子有限公司

Quality Inspection Standards

品质允收标准

Model No. / 产品型号: Applies 0.95~5.0 Inch Touch Display ScreenUpdated Date / 生效日期: 2022-05-20Version / 版本: A0

Customer confirmation : _____

Record of Revision / 修订履历

Version / 版本	Revision Record / 修订内容	Reviser / 修订人	Revision Date / 修订日期
V0	首发 / Starting		2022.05.20

1.Scope of application /适用范围.

This document shall be applied to 0.95~5.0 inch touch display screen.

本文件适用于0.95~5.0 寸触摸显示屏.

2.Inspection conditions and environment /检验条件与环境.

2.1 Inspection Conditions /检验条件:

(1) Inspection Distance /检测距离: 35cm $\pm$ 5cm.

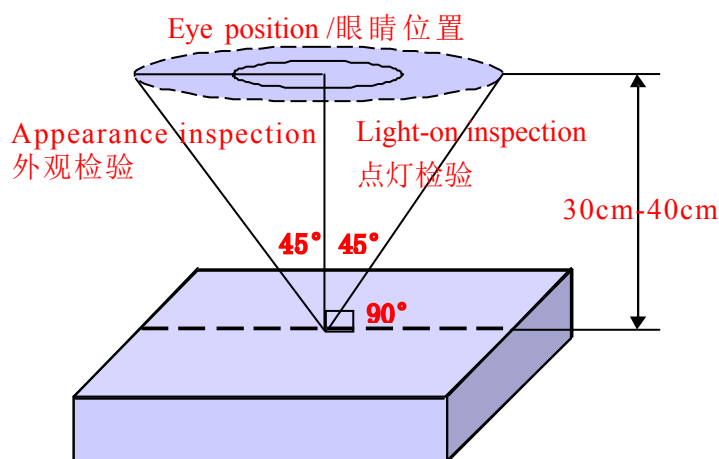
(2) Check time /检验时间:

Displays performance test /功能测试: 2~3S /Image, Cosmetic Inspection /外观检验:10~12S.

(3) Check the viewing angle /检验视角:

Light-on Inspection Angle /点灯检验角度: $\pm 45^\circ$.

Cosmetic Inspection Angle /外观检验角度: $\pm 45^\circ$.



(Perpendicular to LCD panel surface /垂直于LCD表面)

2.2 Inspection environment /检验环境:

Ambient Temperature 温度		25°C $\pm$ 5°C
Ambient Humidity 湿度		55 $\pm$ 5%RH
Ambient Illumination 亮度	Cosmetic Inspection 外观检验	800-1000 Lux
	Functional Inspection 点灯检验	200~300Lux

2.3 Sampling Conditions /抽样条件:

(1) Quantity to be inspected /批量: Quantity of shipment lot per model /单次运送单一型号数量.

(2) Sampling method /抽样方法:

Sampling Plan /抽样计划		GB/T 2828.1- 2003
		Normal Inspection , Single Sampling 正常检验、单次抽样
		General inspection level: II 一般检验水平: 二级
AQL	Major Defect /主要缺陷	0.65
	Minor Defect /次要缺陷	1.0

(3) The classification of Major(MA) and Minor(MI) defects is shown as “3.1 Classification of defects” .
主缺 (MA) 及次缺 (MI) 定义于”3.1缺陷分类”.

3.Terms And Definitions /术语和定义

3.1 Classification of defects / 缺陷分类:

(1) Major defects /主要缺陷:

A major defect is a defect that is likely to result in failure, or to reduce materially the usability of the product for its intended purpose .

可导致产品功能失效或减少产品可用性的缺陷.

(2) Minor defects /次要缺陷:

It will not cause the product to fail and reduce the defects in the effective use and operation of the product.

不会导致产品功能失效和减少产品的有效使用与操作的缺陷.

3.2 Point defects /点状缺陷:

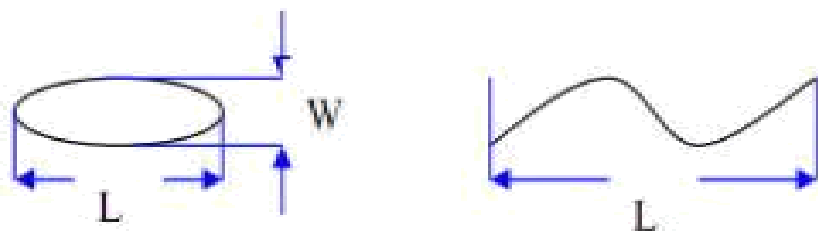
The size of the point defect is defined by the diameter D, and the average diameter of the defect is $D=1/2 (W+L)$.

点状缺陷的大小是由直径 D 定义的, 缺陷的平均直径 $D=1/2(W+L)$.

3.3 Linear defects /线状缺陷:

When defect size $L \geq 2W$, the defect count as liner type defect. Size of linear defect is defined by length (L) and the maximum width (W).

当缺陷尺寸 $L \geq 2W$ 时, 被视为线状缺陷, 线状缺陷是由长度 (L) 和最大宽度 (W) 定义的.



3.4 LCD sub-pixel dot /LCD子像素点

(1) Definition /定义 : The point defect area is greater than 50% of the LCD sub-pixel area, and is visible through ND5% filter masking .

子像素点缺陷面积大于 50% LCD子像素面积, 且透过 ND5%遮盖是可见的.

(2) The drawing of 1/2 area sub-pixel definition / 1/2 面积的子像素定义绘图:

The 1/2 area sub-pixel can be defined as below one or more of specific shapes

1/2 面积的子像素可以定义为如下一个或多个特定形状图:



3.5 Small bright dot /细碎亮点 :

Point defects smaller than "LCD sub-pixels" /小于“LCD子像素点”的点缺陷.

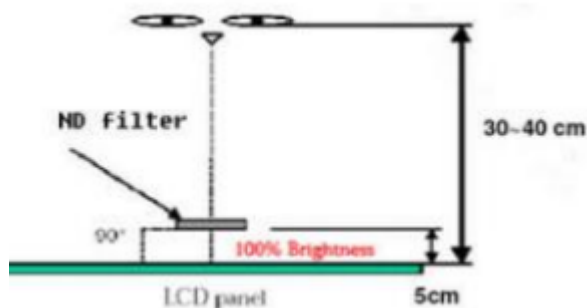
(Ratio of Zone I to Zone O /I 区与 O 区比例: 1: 2: 1)



3.6 ND filter inspection method /ND卡的检验方法:

Hold the ND filter about 5cm above the display area, with your eyes 30-40cm away from the panel, and observe for 2~3 seconds.

在显示区域上方大约 5cm 处握住 ND 卡, 眼睛距离面板 30-40cm, 观察2~3 秒.



3.7 Any FPC surface problems that do not leak copper on the surface and do not cause functional failure are acceptable.

任何 FPC 表面问题, 表面未露铜和不造成功能失效是可以接受的.

3.8 Extraneous substances that can be wiped out , like Finger point,Particles are not considered as a defect .

可以被擦拭干净的表面物质不视为缺陷 (如手指印, 尘粒) .

3.9 Defects that are covered by the material and are not visible to the eye and do not affect the function and use are not considered defects.

会被物料覆盖目视不可见，且不影响功能与使用的缺陷不视为缺陷。

3.10 Panel damage /面板损伤:

Glass damage outside the AA display area that does not affect the effective wiring is acceptable.

AA 显示区域以外的玻璃损伤，不影响有效线路是可以接受的。

3.11 Issues not specified or defined in this acceptance standard shall be handled through friendly negotiation between the two parties.

本允收标准中未规定或定义的问题，双方友好协商处理。

4. Inspection standards /检验标准

4.1 Structural Dimensions /结构尺寸规格

Serial Number 序号	Measurement items /测量项目		Specification /规格	Remark /备注
	名称 /Name	Unit /单位	Tolerance /公差	
1	Outside dimension: Length 尺寸: 长	mm /毫米	0.10mm~0.20mm	Please refer to the product specification for detailed dimensions and tolerances 详细的尺寸规格和公差请参考产品规格书
2	Outside dimension: Width 尺寸: 宽	mm /毫米	0.10mm~0.2mm	
3	Outside dimension: Thickness 尺寸: 厚	mm /毫米	0.20mm~0.30mm	

4.2 Appearance Inspection Specification /外观检验规格

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass 玻璃	Wire(on Array) 线路	Can't be damaged 不能损伤	MA	
	Chipping/corner breaking 崩边/破角	Can't affect the effective lines and functions 不能影响有效线路和功能	MA	
	Edge 边缘	There must be no extensional cracks 不可有延伸性裂纹	MA	
Silicone 硅胶	Silicone coating 硅胶涂布	The height must not exceed the LCD CF surface 高度不能超过LCD CF面		MI
	Glue overflow 溢胶	Can't cover FPC, POL, etc 不能覆盖到FPC、POL等		MI

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
PCBA FPC Connector 连接器	Appearance 外观	Scratches or injuries are not allowed to cause copper exposure 划伤或损伤不允许表面出现露铜		MI
	Component 元器件	Can't be damaged and lack 不能损伤和缺少	MA	
	Gold finger oxidation 金手指氧化	Not allowed 不允许		MI
	Connection status 连接状况	The connection must be accurate and stable 必须准确稳定连接	MA	
	Break 破裂	Not allowed 不允许	MA	
	Soldering, false soldering/tinning/tin beads 假焊/连锡/锡珠	Not allowed 不允许	MA	
POL 偏光片	Scratches 划伤	1. $W \leq 0.05\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.05\text{mm} < W \leq 0.10\text{mm}$; $L \leq 5\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.10\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许)		MI
	Dent 凹凸印	1. $D \leq 0.15\text{mm}$, Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$, Not allowable (不允许)		MI
	Bubbles 气泡	1. $D \leq 0.15\text{mm}$, Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$, Not allowable (不允许)		MI
	Point defects 点状不良	1. $D \leq 0.15\text{mm}$, Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$, Not allowable (不允许)		MI
	Edge bubbles 边缘气泡	1. Within 1/2BM of the display area, it is not allowed 显示区往外 1/2BM 区域内, 不允许 2. The display area is 1/2 outside the BM area, and it is not controlled 显示区往外 1/2BM 区域以外, 不管控		MI
	Dirty/watermarked 脏污/水印	No dirt/water lines/finger marks are allowed, and must be wiped clean 不允许有脏污/水印/手指印, 须擦拭干净方可		MI
	Warping 起翘	Not allowed 不允许		MI
	Attaching offset 贴偏	It is necessary to completely cover the display area outward, within the 1/2BM area, or without leaking POL edges after TP is attached 需完整覆盖显示区往外、1/2BM 区以内或贴合 TP 后不会出现漏偏光片边缘		MI
	Mixture 混料	Mixing different types of POL or not using POL as required by the BOM, not allowed 不允许混贴不同型号的 POL 或未按 BOM 要求使用 POL	MA	

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
TP&CG	Point defects 点状不良	1. $D \leq 0.15\text{mm}$, Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$, Not allowable (不允许)		MI
	Scratches 划伤	1. $W \leq 0.05\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.05\text{mm} < W \leq 0.10\text{mm}$; $L \leq 5\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.10\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许) 4. There is a feeling scratch, Not allowable 有感划伤, 不允许		MI
	Edges and corners cracked 崩角/崩边	1. Product front / 产品正面: Edge and corner chipping is not allowed 崩角、崩边不允许 2. Product back / 产品背面: $X \leq 0.5$, $Y \leq 0.5$, $Z \leq 1/2T$; $N \leq 3$; $DS \geq 10\text{mm}$		MI
	Silk screen 丝印	The silk screen is clear, complete and correct 丝印清晰、完整、内容正确		MI
	Dirty 脏污	Non-wipeable dirt, not allowed 不可擦拭的脏污, 不允许		MI
	Broken 破损	Not allowable 不允许	MA	
	Ink color aberration 油墨色差	$\Delta E > 1$, Not allowable (不允许)		MI
	Cover pinholes 针孔	1. $D \leq 0.10\text{mm}$, $N \leq 3$, $DS \geq 10\text{mm}$, allowable 2. $D > 0.10\text{mm}$, intensive pinholes (密集型针孔), Not allowable (不允许)		MI
	IR holes IR孔	Dirt, deviation, color difference, etc. are not allowed 不允许脏污、偏位、色差等		MI
BL 背光	Backlight separation 背光分离	Not allowable 不允许		MI
	Deformation of rubber iron and rubber frame 胶铁、胶框变形	Use the plug gauge 0.3mm on the flat surface and can snap in and judge NG 在平面上使用塞规0.3mm卡翘曲位置, 能卡进判定NG		MI
	The iron frame is oxidized and not tightened 铁框氧化、卡不紧	Not allowable 不允许		MI
	Backlight sticky solder beads, glue, etc 背面粘锡珠、残胶等	Not allowable 不允许		MI
	Lnkjet coding, Barcode, QR code 喷码/条码/二维码	The lmkjet coding is clear and complete, the barcode and QR code can be scanned normally, and the content and format match 喷码清晰完整、条码和二维码可正常扫描, 内容和格式相符		MI
	Accessories (protective film, double-sided tape, insulating adhesive, etc.) 辅料(保护膜、双面胶、绝缘胶等)	Defects such as missing pastes, sticking deviations, defects, and fractures are not allowed 不允许有漏贴、贴偏、残缺、断裂等缺陷		MI

4.3 Electrical test specifications /电性检查规格

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass bright spots/dark spots 玻璃亮点/暗点	1. $D \leq 0.15\text{mm}$, Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$, Not allowable (不允许)		MI
Mura	Use ND5% filter masking, visual invisibility is OK, 200~300Lux 使用ND5%遮盖, 目视不可见即为OK, 200~300Lux		MI
Small bright dot 细碎亮点	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
Light leakage 漏光	1. Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK 2. If necessary, sign off on the sample 必要时, 签限定样		MI
Backlight black/white dots 背光黑点/白点	1. $D \leq 0.15\text{mm}$, Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$, Not allowable (不允许)		MI
Linear foreign bodies 线状异物 (异物毛丝等)	1. $W \leq 0.05\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.05\text{mm} < W \leq 0.10\text{mm}$; $L \leq 5\text{mm}$; $N \leq 3$; $DS \geq 10\text{mm}$ 3. $0.10\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许)		MI
Black/White Print 黑印/白印	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The display is uneven 显示不均匀	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The brightness is uneven 亮度不均匀	Brightness uniformity $< 85.0\%$, Not allowable 亮度均匀性 $< 85.0\%$, 不允许		MI
Displacement of the membrane 膜材移位	Not allowable 不允许		MI
Interference pattern/Newtonian pattern 干涉纹/牛顿纹	Not allowable 不允许		MI
Display abnormal 显示异常	Not allowable 不允许	MA	
No display 无显示	Not allowable 不允许	MA	
Line/Missing Drawing 线条/缺画	Not allowable 不允许	MA	
Splash screen 闪屏	Not allowable 不允许	MA	
LCD grid LCD网格	Not allowable 不允许	MA	
Afterimage 残影	Not allowable 不允许	MA	
Wrong viewing angle 视角错误	Not allowable 不允许	MA	
No touch 无触摸	Not allowable 不允许	MA	
Touch the jump point 触摸跳点	Not allowable 不允许	MA	
Not sensitive 触摸不灵敏	Not allowable 不允许	MA	